


**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

 Form PTO-1449 (Modified)
(Use several sheets if necessary)

COMPLETE IF KNOWN

Application Number	10/651,913
Confirmation Number	9569
Filing Date	August 29, 2003
First Named Inventor	Eric Tan Swee Seng
Group Art Unit	2826
Examiner Name	Alexander O. Williams
Attorney Docket No.	108298733US

Sheet 1 of 7

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	U.S. Patent or Application		Name of Patentee or Inventor of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			
[Signature]		4,012,579		Fox et al.	03/15/1977	
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EXAMINER

DATE CONSIDERED

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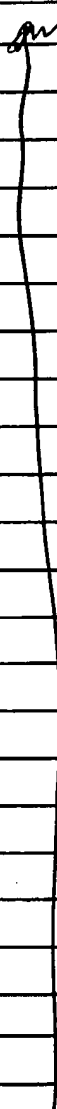

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EXAMINER <i>[Signature]</i>	DATE CONSIDERED <i>4/2/04</i>
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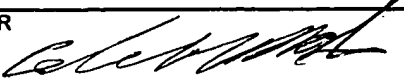
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Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published.	T
<i>AS</i>		CARSON, John C., "Advances in Chip Level Packaging," Irvine Sensors Corporation, 36 pages, retrieved from the Internet on July 17, 2003, < http://www.ece.jhu.edu/faculty/andreou/495/2002/LectureNotes/PackagingAdvancedpdf.pdf >.	
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